

FIG.1

FIG.2



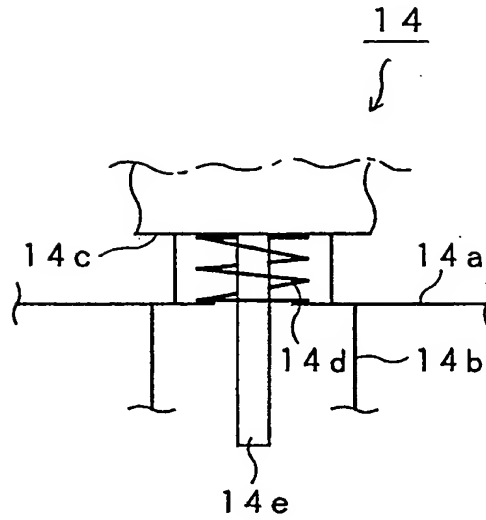


FIG. 4A

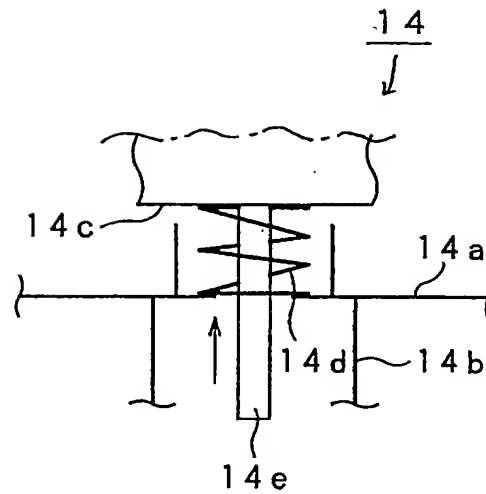


FIG. 4B

FIG. 5



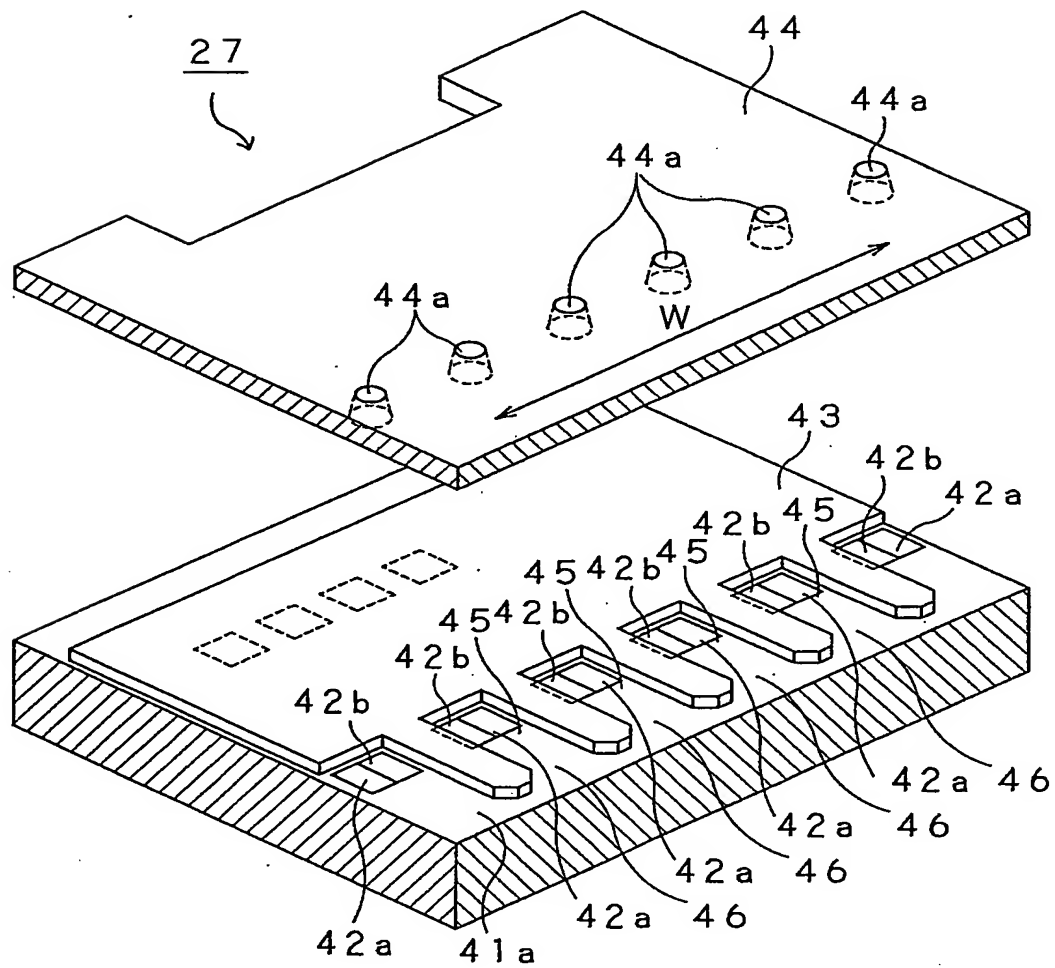


FIG. 7

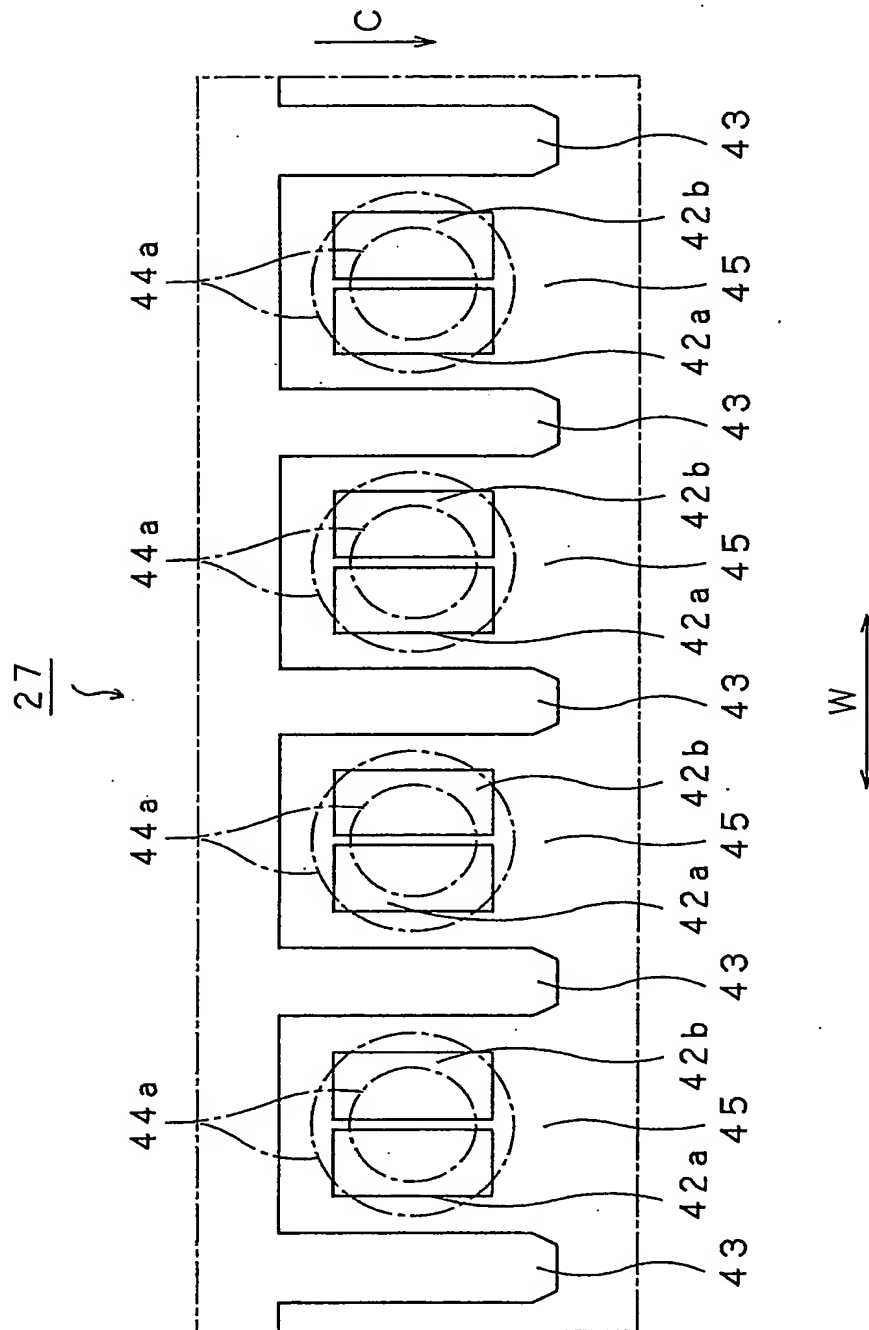


FIG.8



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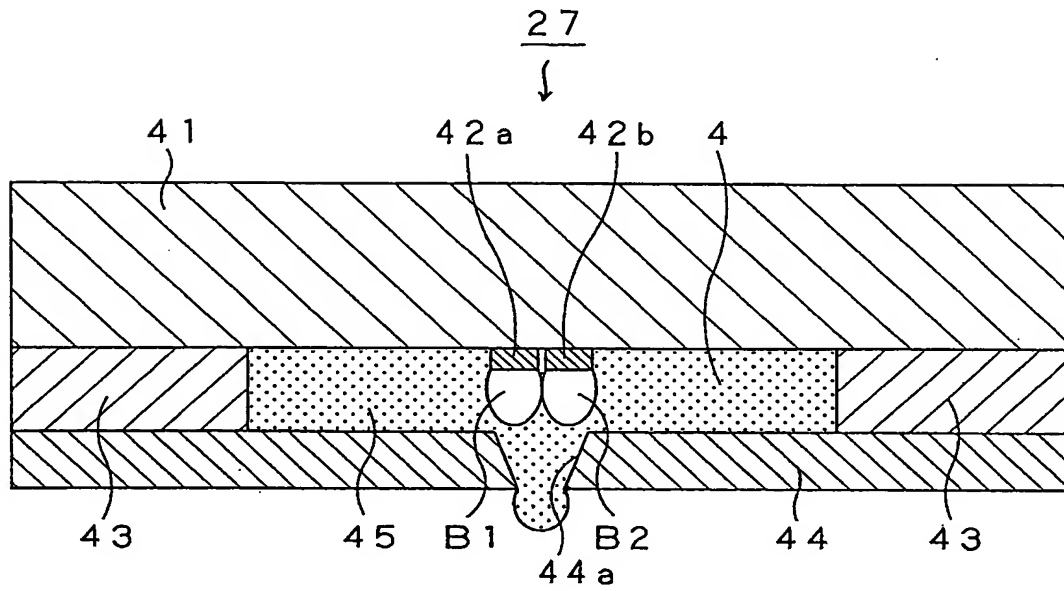


FIG.9

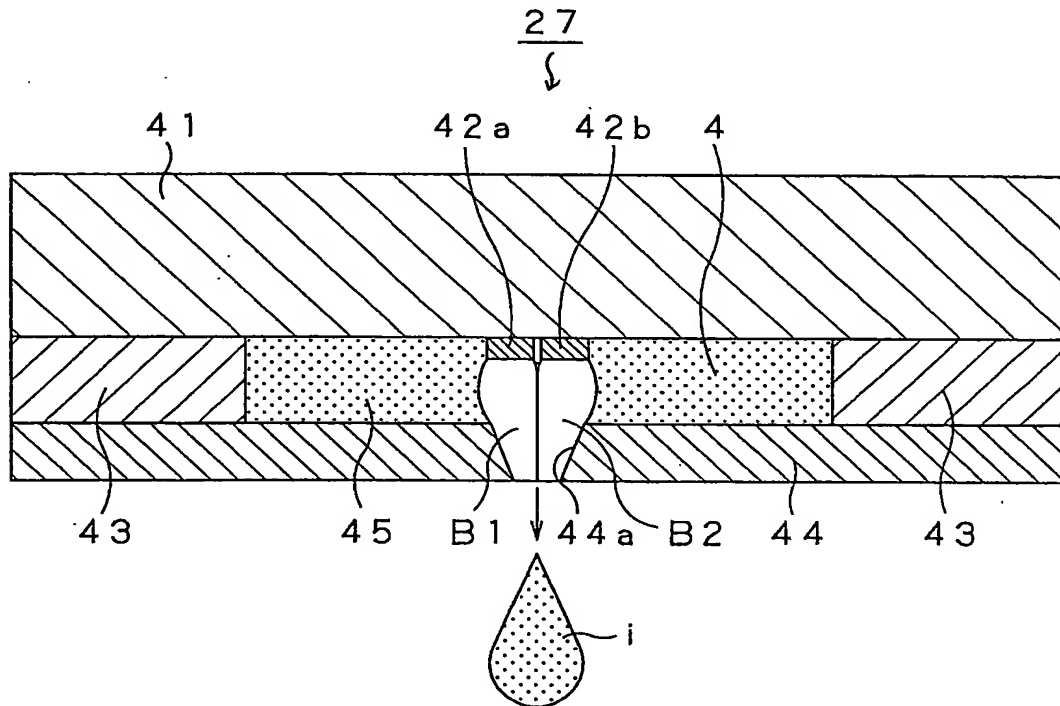


FIG.10



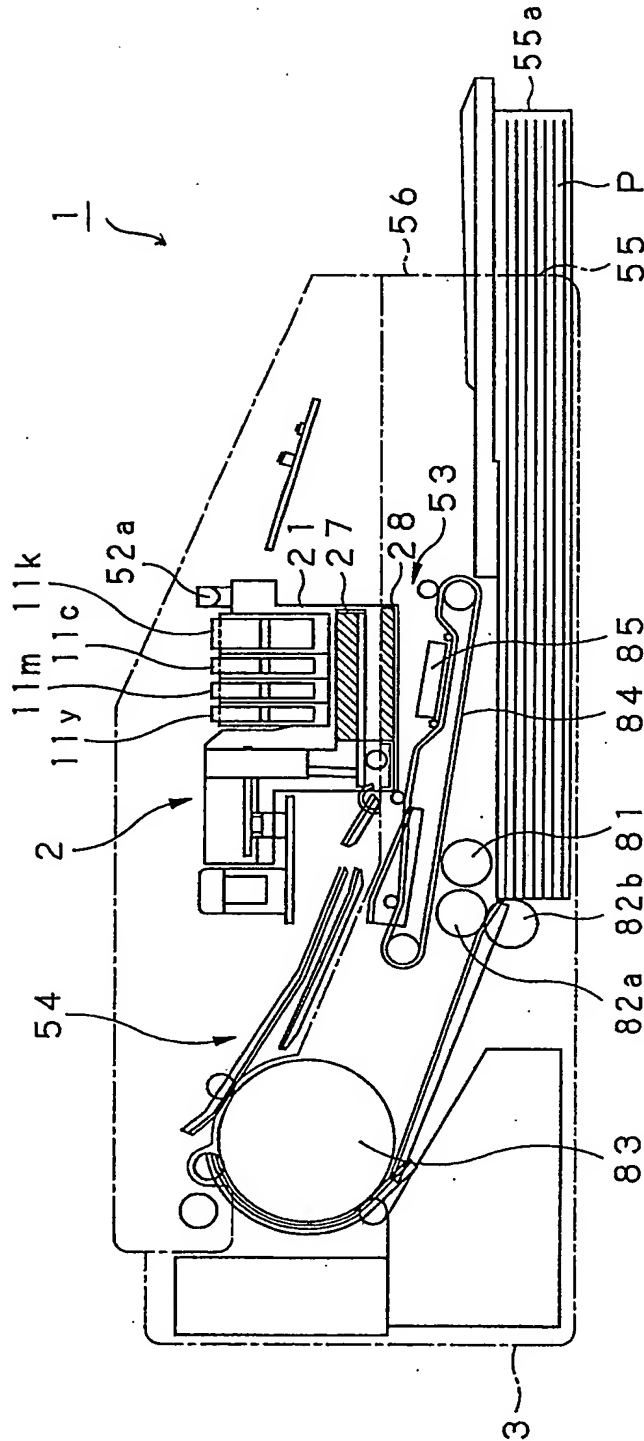


FIG.13

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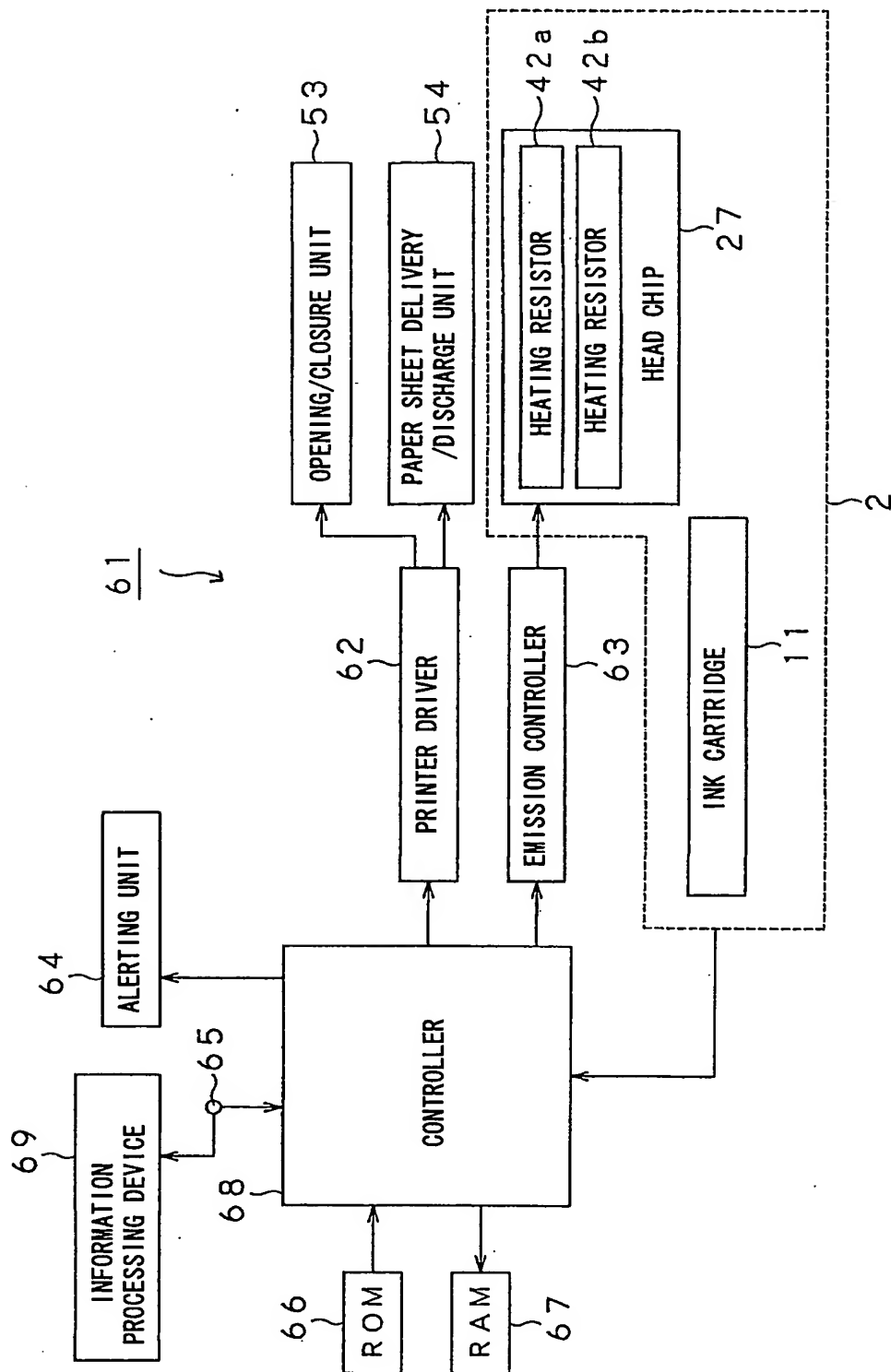


FIG.14

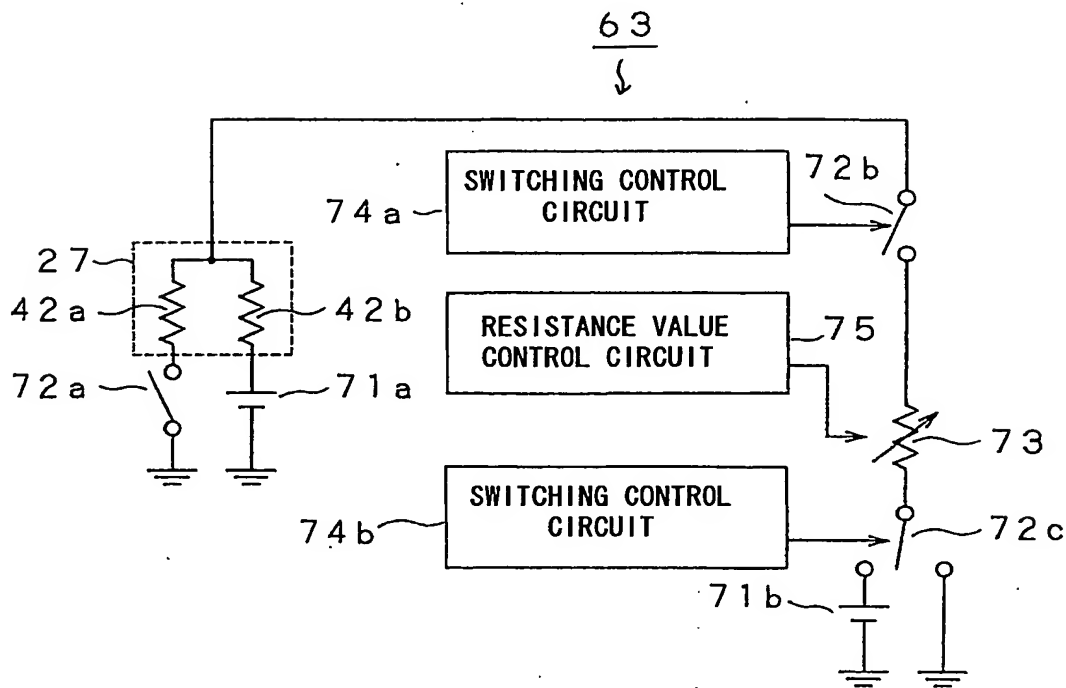


FIG.15

A cross-sectional diagram of a liquid crystal display device. It shows a substrate 27 with a central layer 4. Layer 4 contains several regions: 41 (diagonal hatching), 42a (dotted pattern), 42b (vertical hatching), and 43 (horizontal hatching). A layer 45 is located between regions 41 and 42a. The bottom surface of the substrate is labeled 27a. A droplet i is shown at the bottom surface 44a, and a pressure P is indicated by an arrow pointing upwards from below.

Fig. 1 is a cross-sectional view of a semiconductor device. The device consists of a substrate 27. On the top surface of the substrate 27, there is a layer 41. Below layer 41, there is a layer 42. Layer 42 contains two regions, 42a and 42b, which are separated by a barrier layer 44. A central region 43 is located between the barrier layers 44. A layer 45 is located between layer 41 and layer 42. The device is shown with a width W and a depth P.

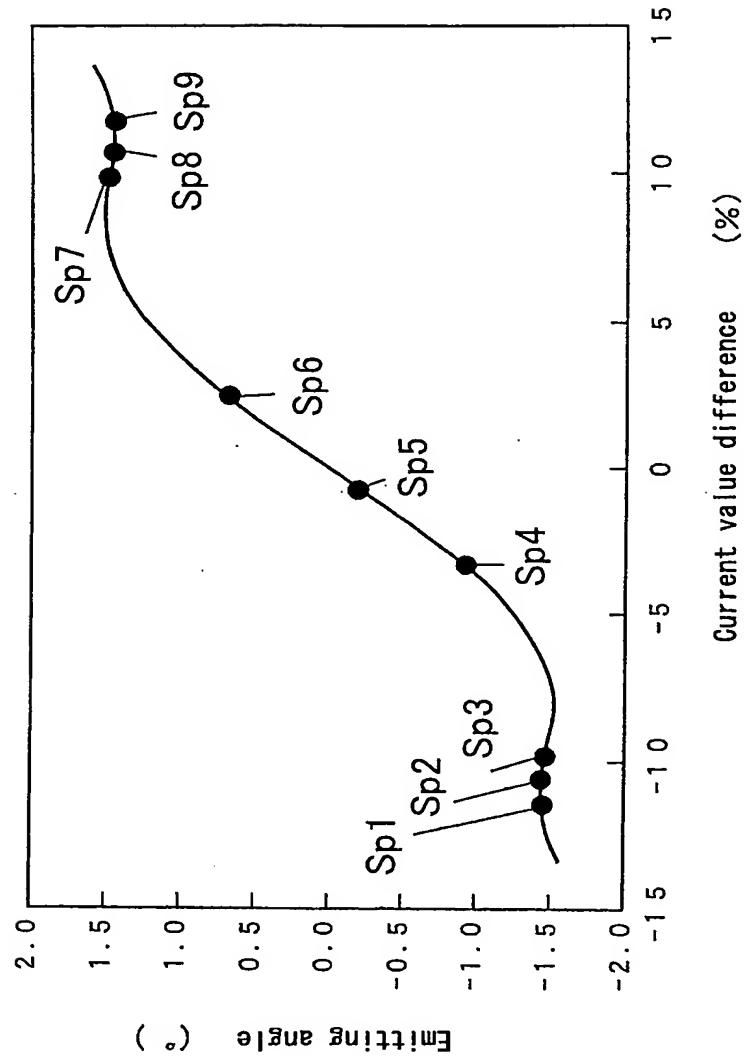


FIG.17

FIG.18A

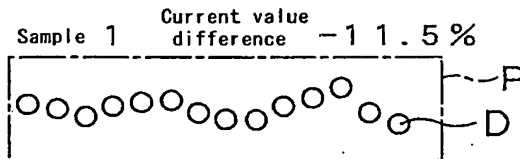


FIG.18B

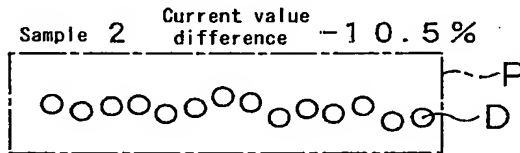


FIG.18C

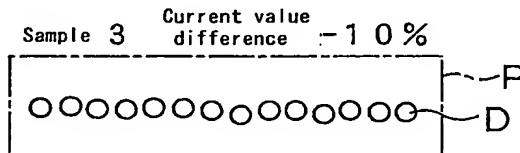


FIG.18D

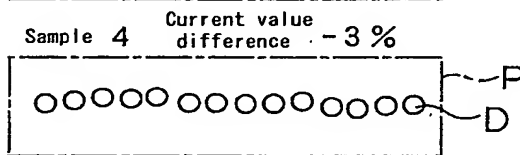


FIG.18E

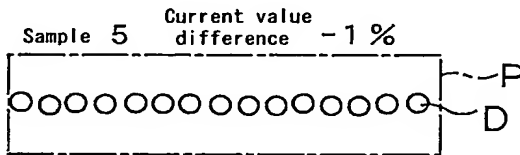


FIG.18F

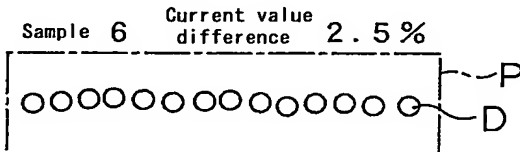


FIG.18G

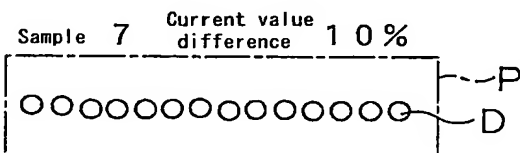


FIG.18H

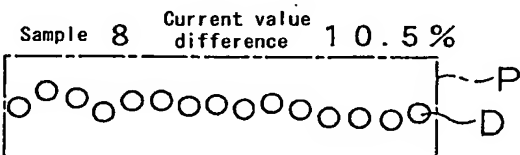
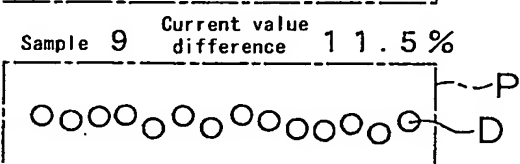


FIG.18I





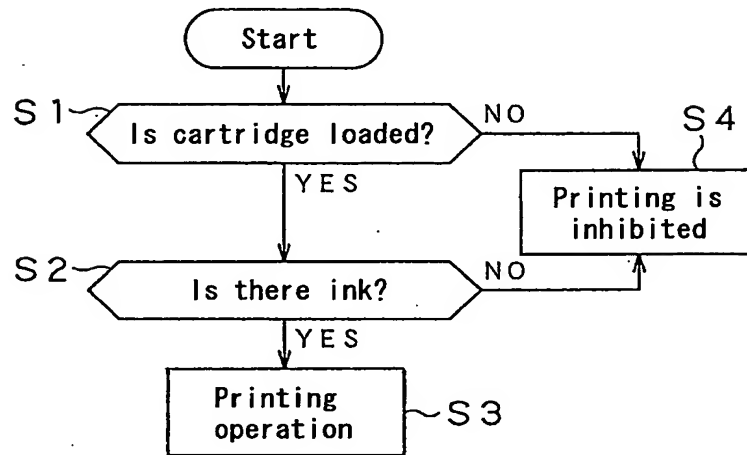


FIG.19

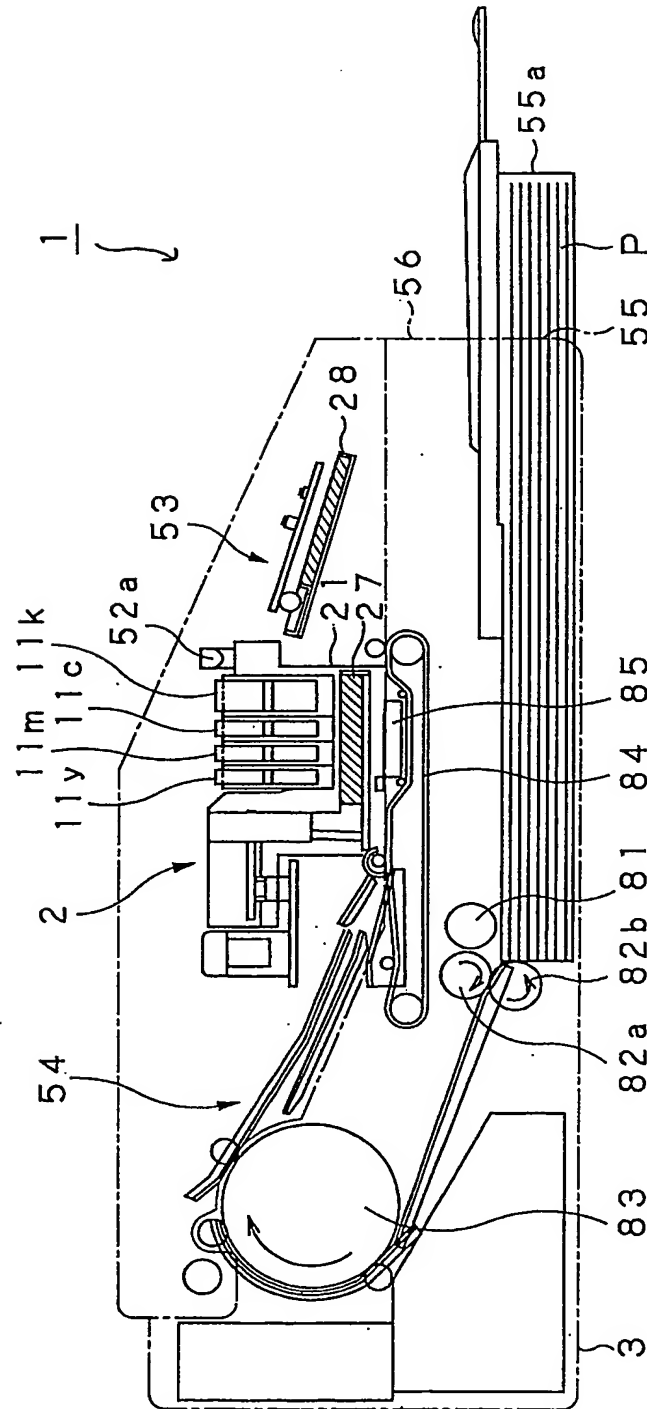


FIG. 20

FIG.21A

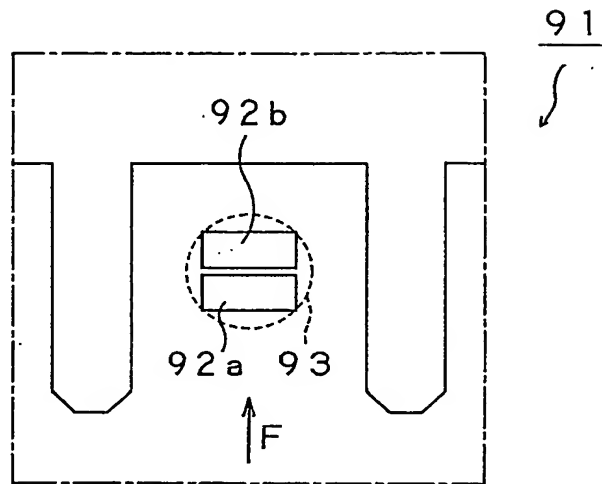


FIG.21B

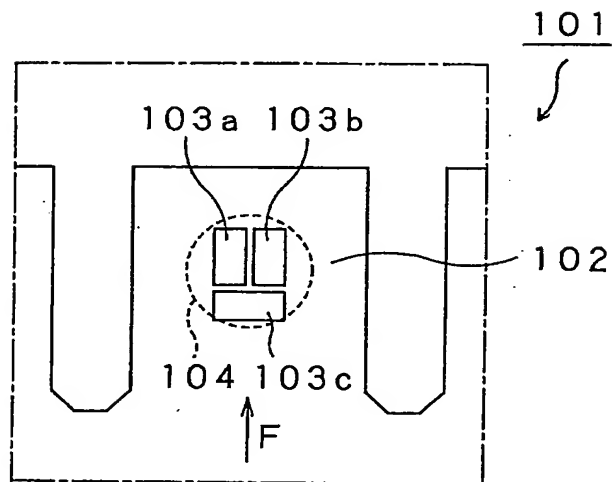
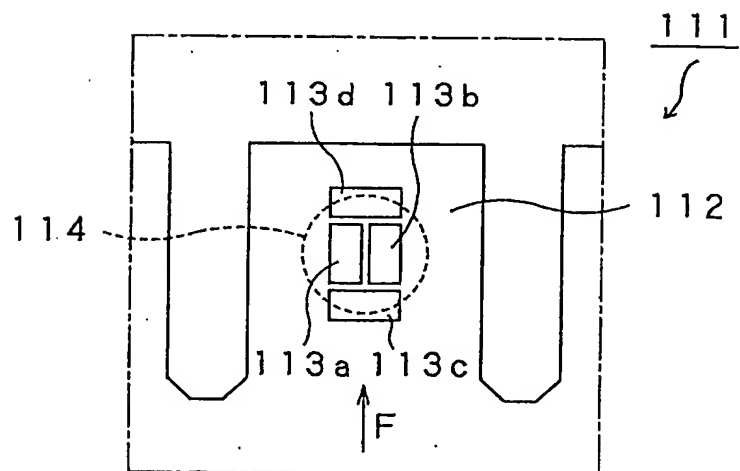


FIG.21C



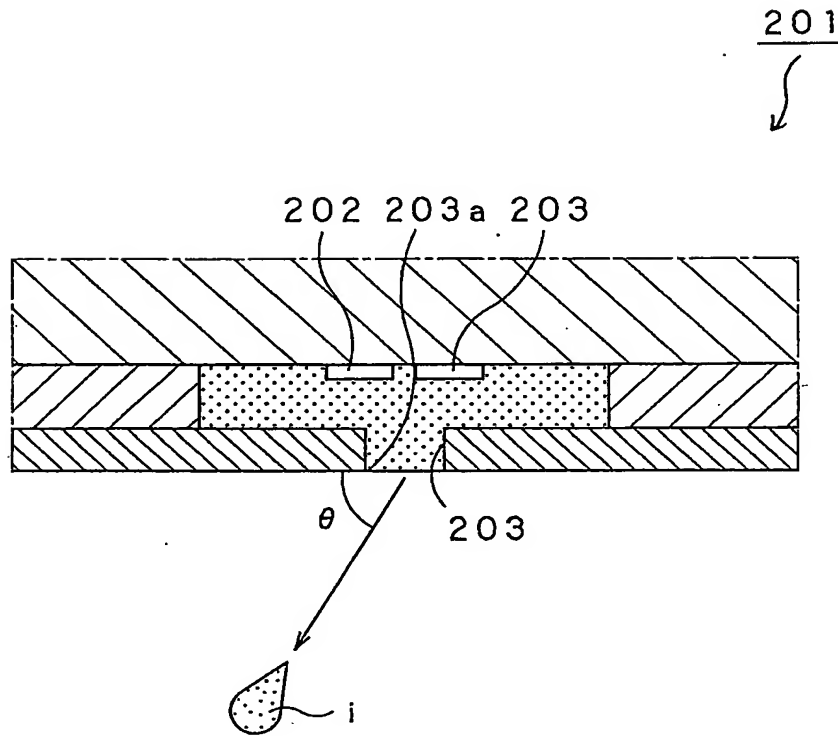


FIG.22